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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application Number	09/827,056
		Filing Date	April 3, 2001
		First Named Inventor	Liu, Chun-Mai
		Art Unit	2811
		Examiner Name	Thomas J. Magee
Total Number of Pages in This Submission	23	Attorney Docket Number	018757-004600US

ENCLOSURES (Check all that apply)		
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Signature	<i>[Signature]</i>
Date	3/18/03

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TOWNSEND and TOWNSEND and CREW LLP

By: Marianne C. C. C.

10/13(N.E)
J. Steptoe
3-27-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Chun-Mai Liu et al.

Application No.: 09/827,056

Filed: April 3, 2001

For: METHOD OF FABRICATING
HIGH-COUPLING RATIO SPLIT GATE
FLASH MEMORY CELL ARRAY

Examiner: Thomas J. Magee

Art Unit: 2811

AMENDMENT

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Assistant Commissioner for Patents
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Sir:

In response to the Office Action mailed 12/30/02, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend the claims 1, 5, 6, 12 and 14 as follows:

- 556C' >
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1. (Twice amended) A method of fabricating a flash memory device including an array of split-gate cells, comprising the steps of:
providing a silicon substrate having a top surface;
forming a common source region in an area of said top surface for each pair of said cells;
implanting ions into predefined areas of each said common source region;